

TIME: 40 minutes

MAXIMUM MARKS: 30

SEM	SET	PAPER CODE	TITLE OF THE PAPER
II	2014	14PPH2107	MICROPROCESSOR AND MICROCONTROLLER

SECTION - A**Answer all the questions:****30 × 1 = 30****Choose the correct answer:**

- The flag that is not available for the programmer, In 8085 microprocessor is
 - Carry
 - Sign
 - Zero
 - Auxiliary carry
- The OPCODE for MOV A, B in 8085 microprocessor is
 - 7A
 - 7E
 - 06
 - 78
- The instruction from where the next instruction will be picked up for the TRAP interrupt in 8085 microprocessor is
 - 0024
 - 002c
 - 0034
 - 003c
- In 8085, for an OPCODE fetch
 - $S_1 = S_0 = 0$
 - $S_1 = 0, S_0 = 1$
 - $S_1 = 1, S_0 = 0$
 - $S_1 = S_0 = 1$
- In 8085, the STACK works on _____ principle.
 - LIFO
 - FIFO
 - FILO
 - LIFO

6. When we do shift left the 8-bit data's numerical value
- Increases by 8
 - Increases by 2
 - Decreases by 8
 - Decreases by 2
7. 8C00 MVi A, 3E
8C02 MVi B, 06
8C04 HLT
- In the above 8085- Assembly language program, what will be the content of C-Register?
- 3E
 - 06
 - 3E-06
 - any value
8. A mini program that is often used by the main program is called _____
- Subroutine
 - Mnemonics
 - opcode
 - t-states
9. LDAX B comes under _____ addressing mode.
- Register indirect
 - Direct
 - Immediate
 - Implicit
10. In a multi byte addition program, it is better to
- Increase the data length by 1 byte
 - Increase the number of data to be added by 1
 - Both a & b
 - Neither a nor b
11. Micrr-controller 8051 has _____ I/O pins.
- 8
 - 24
 - 32
 - 64
12. Reade the following MC-8051 program carefully and answer.
MOV A, #38H
ADD A, #2FH. After the addition, the parity flag's value would be _____.
- p = 10
 - 01
 - 1
 - 0
13. In 8051, "SETB PSW.3" will make
- PSW.3 =1
 - Select bank register 1
 - Both a & b
 - None of these

22. Which one is the correct answer? In 8051, upon reset, the priority is
a) Highest for serial communication b) least for INTO
c) All have the same priority d) None of these
23. 8255 has _____ I/O ports.
a) 1 b) 2 c) 0 d) 3
24. The control word with the MSB = 0 is meant for _____
a) I/O mode b) Input mode
c) BSR mode d) Output mode
25. Among the following, which one is the common-cathode 7segment display _____
a) FND 507 b) FND 503 c) FND 510 d) MAN 72
26. While using ADC 0809 with 8085 via 8255, if the EOC is received at PB₃ then how many RAR will be used to identify the carry?
a) 1 b) 2
c) 3 d) 4
27. The instruction that is used to get the decimal output while using 8085 is
a) ADD b) DAD
c) DAA d) LDA
28. While interfacing a LCD with 8051, which one is correct?
a) RS pin is an input b) RS pin is an output
c) E pin is an input d) Both a & c
29. For a NORMAL 4-step sequence of a stepper motor, the sequence next to 1001 for clockwise rotation is
a) 1100 b) 0110
c) 0011 d) 1100
30. The output of DAC 808 is
a) Current b) Voltage
c) Binary d) Decimal
