## St. JOSEPH'S COLLEGE (AUTONOMOUS) TIRUCHIRAPPALLI – 620 002 SEMESTER EXAMINATIONS – APRIL 2015

TIME: 40 minutes

**MAXIMUM MARKS: 30** 

SEM	SET	PAPER CODE	TITLE OF THE PAPER
II	2014	14PPH2107	MICROPROCESSOR AND MICROCONTROLLER

## **SECTION - A**

## **Answer all the questions:**

 $30 \times 1 = 30$ 

Choose the correct answer:

1.	The	flag	that	is	not	available	for	the	programmer,	In	8085
	micr	oproc	essor	is							

a) Carry

b) Sign

c) Zero

d) Auxiliary carry

2. The OPCODE for MOV A, B in 8085 microprocessor is

a) 7A

b) 7E

c) 06

d) 78

3. The instruction from where the next instruction will be picked up for the TRAP interrupt in 8085 microprocessor is

a) 0024

b) 002c

c) 0034

d) 003c

4. In 8085, for an OPCODE fetch

a)  $S_1 = S_0 = 0$ 

b)  $S_1 = 0$ ,  $S_0 = 1$ 

c)  $S_1 = 1$ ,  $S_0 = 0$ 

d)  $S_1 = S_0 = 1$ 

5. In 8085, the STACK works on \_\_\_\_\_ principle.

a) LILO

b) FIFO

c) FILO

d) LIFO

6.	When we do shift left the 8-bit data's numerical value
	a) Increases by 8 b) Increases by 2
	c) Decreases by 8 d) Decreases by 2
7.	8C00 MVi A, 3E
	8C02 MVi B, 06
	8C04 HLT
	In the above 8085- Assembly language program, what will be the
	a) 3E b) 06 c) 3E-06 d) any value
8.	A mini program that is often used by the main program is
0.	called
	a) Subroutine b) Mnemonics
	c) opcode d) t-states
9.	LDAX B comes under addressing mode.
	a) Register indirect b) Direct
	c) Immediate d) Implicit
10.	In a multi byte addition program, it is better to
	a) Increase the data length by 1 byte
	b) Increase the number of data to be added by 1
	c) Both a & b d) Neither a nor b
11.	Micrr-controller 8051 hasI/O pins.
	a) 8 b) 24 c) 32 d) 64
12.	Reade the following MC-8051 program carefully and answer.
	MOV A, #38H
	ADD A, #2FH. After the addition, the parity flag's value
	would be
12	a) $p = 10$ b) $01$ c) $1$ d) $0$
13.	In 8051, "SETB PSW.3" will make
	a) PSW.3 = 1 b) Select bank register 1
	c) Both a & b d) None of these

14.	For an 8051 system of 11.0592 MHz, the ratio between time taken for LJMP and SJMP is
15	a) 1:2 b) 2:1 c) 1 d) 2:4
15.	After executing the following program,
	SETB PSW.4
	MOV R0, #99H
	MOV R1, #85H
	MOV R7, #63H
	MOV R5, #12H pick out the wrong answer.
	a) RAM location 10 has 99H b) RAM location 12 has 85H
	c) RAM location 17 has 63H d) RAM location 15 has 12H
16.	8051 has timers. And each timer has registers.
	a) 8,2 b) 4,2 c) 2,4 d) 2,8
17.	Find the odd one out:
	a) The transfer of data using parallel lines is faster
	b) The transfer of data using parallel lines is more expensive
	c) Sending data to a printer is duplex
	d) In full duplex, we must have to data lines
18.	
	a) Voltage b) Current
	c) Digital d) None of these
19	TRI is set to start timer
1).	a) 2 b)1 c) 4 d) 3
20	In 8051, the address assigned in the interrupt vector table for
20.	timer 0 is
	a) 000AH b) 000BH c) 000CH d) 000DH
21.	Including reset, how many interrupts do we have in 8051?
<b>~</b> 1.	a) 4 b) 5
	,
	c) 6 d) 7

22.	Which one is the correct answer	er? In 8051, upon reset, the priority is
	a) Highest for serial commun	nication b) least for INTO
	c) All have the same priority	d) None of these
23.	8255 hasI/O ports.	
	a) 1 b) 2 c) 0	d) 3
24.	The control word with the MS	BB = 0 is meant for
	a) I/O mode	b) Input mode
	c) BSR mode	d) Output mode
25.	Among the following, which display	one is the common-cathode 7segmen
	a) FND 507 b) FND 503 d	c) FND 510 d) MAN 72
26.	_	8085 via 8255, if the EOC is received vill be used to identify the carry?
	a) 1	b) 2
	c) 3	d) 4
27.	The instruction that is used to 8085 is	o get the decimal output while using
	a) ADD	b) DAD
	c) DAA	d) LDA
28.	While interfacing a LCD with	8051, which one is correct?
	a) RS pin is an input	b) RS pin is an output
	c) E pin is an input	d) Both a & c
29.	For a NORMAL 4-step sequenext to 1001 for clockwise rot	ence of a stepper motor, the sequence ation is
	a) 1100	b) 0110
	c) 0011	d) 1100
30.	The output of DAC 808 is	
	a) Current	b) Voltage
	c) Binary	d) Decimal